

	09:00 10:00	10:00 11:00	11:00 12:00	12:00 13:00	13:00 14:00	14:00 15:00	15:00 16:00	16:00 17:00	17:00 18:00
<b>Monday</b>			<p>Lecture, Wks 23-24, 02/03/2020 - 09/03/2020</p> <p>Module: ELEC40002 - Analysis and Design of Circuits Group: <u>EE-EEE1 Year Group</u> Staff: <u>Holmes, Andrew S</u>; Lan, Lingxin; Mitcheson, Paul</p> <p>Room: <u>EENG 408</u></p>			<p>Laboratory Session, Wks 23-24, 02/03/2020 - 09/03/2020</p> <p>Module: ELEC40002 - Analysis and Design of Circuits Group: <u>EE-EEE1 A1</u> Staff: Lan, Lingxin; Mitcheson, Paul D Room: <u>EENG 114 - Teaching Lab</u></p>		<p>Academic Tutorial, Wks 23-25, 02/03/2020 - 16/03/2020</p> <p>Module: EE1 Tutorial Groups: EE-EEE1 PDMT BE (Wk 24); EE-EEE1 PDMT BO (Wks 23,25) Staff: Mitcheson, Paul D Room: EENG 1112</p>	
<b>Tuesday</b>	<p>Lecture, Wks 23-24, 03/03/2020 - 10/03/2020</p> <p>Module: ELEC96027 - Power Electronics Groups: EE-EEE3 - 3.14PEM; EE-EEE4 - 3.14PEM Staff: Mitcheson, Paul D Rooms: <u>EENG 403A</u>; <u>EENG 403B</u></p>						<p>Academic Tutorial, Wks 23-25, 03/03/2020 - 17/03/2020</p> <p>Module: EE1 Tutorial Groups: EE-EEE1 PDMT AE (Wk 24); EE-EEE1 PDMT AO (Wks 23,25) Staff: Mitcheson, Paul D Room: EENG 1112</p>		

	09:00 10:00	10:00 11:00	11:00 12:00	12:00 13:00	13:00 14:00	14:00 15:00	15:00 16:00	16:00 17:00	17:00 18:00	
<b>Tuesday</b>				<p>Class, Wks 23-25, 03/03/2020 - 17/03/2020</p> <p>Module: ELEC40002 - Analysis and Design of Circuits Groups: <u>EE-EEE1 A1</u>; <u>EE-EEE1 A2</u> Staff: Lan, Lingxin; Mitcheson, Paul D; <u>Wright, Steven W</u> (Wks 23,25) Room: <u>EENG 305 - Level 3 Large Computer Room</u></p>						
<b>Wednesday</b>			<p>Class, Wk 23, 04/03/2020</p> <p>Module: ELEC96027 - Power Electronics Groups: <u>EE-EEE3 - 3.14PEM</u>; <u>EE-EEE4 - 3.14PEM</u> Staff: Mitcheson, Paul D Room: <u>EENG 403A</u></p>							

	09:00 10:00	10:00 11:00	11:00 12:00	12:00 13:00	13:00 14:00	14:00 15:00	15:00 16:00	16:00 17:00	17:00 18:00
Thursday			<p>Lecture, Wks 23-24, 05/03/2020 - 12/03/2020</p> <p>Module: ELEC40002 - Analysis and Design of Circuits Group: <u>EE-EEE1 Year Group</u> Staff: <u>Holmes, Andrew S</u>; Lan, Lingxin; Mitcheson, Paul</p> <p>Room: <u>EENG 408</u></p>	<p>Class, Wks 23-25, 05/03/2020 - 19/03/2020</p> <p>Module: ELEC40002 - Analysis and Design of Circuits Groups: <u>EE-EEE1 B1</u>; <u>EE-EEE1 B2</u> Staff: <u>Holmes, Andrew S</u>; Lan, Lingxin; Mitcheson, Paul D; <u>Wright, Steven W</u> (Wks 23,25) Room: <u>EENG 305 - Level 3 Large Computer Room</u></p>					

	09:00 10:00	10:00 11:00	11:00 12:00	12:00 13:00	13:00 14:00	14:00 15:00	15:00 16:00	16:00 17:00	17:00 18:00
<b>Friday</b>	Lecture, Wks 23-24, 06/03/2020 - 13/03/2020 Module: ELEC40002 - Analysis and Design of Circuits Group: <u>EE-EEE1 Year Group</u> Staff: Lan, Lingxin; Mitcheson, Paul D Room: <u>EENG 408</u>								